

Gnusic included an array of 4x4 DSP16s. The schematic sheets are:

- bigmem A dual ported memory scheme where the run bit of the DSP shut off processor access.
- busi The gnusic bus interface (really an extended gnot interface)
- dsp The generic DSP16
- generic generic was the top level gnusic "core" with a central DSP-16 in the middle. The DSP-16 served as a master of ceremonies.
- memlogic DSP16[slave memory (static expensive memory)
- memory Similar dual ported memory scheme
- piobus Parallel I/O bus interface - notice how the slave DSP16s are in this macro. Also note that the serial I/O was intended to be connected as a ring.
- slave How to connect a DSP16 to a slave dual port memory

















