

The fundamental DSP.\* processor module includes two DSP-32s (not the C version) with serial outputs. Control was done over the VME bus.

bank 4 chip static memory for the DSP

busctl,clocks Various glue logic

dsp DSP-32 connected to the memory

io I/O was done by a slave DSP-32 with differential serial I/O

mb Memory bank switching scheme (the 940 was always in my mind)

pm unreadable top level macros, but connects the VME interface to the chips to the I/O

vme VME interface via the old fashioned Motorola 68172 chip. These day's you'd use an FPGA or one of the newer VME chip sets.













